

# SIMRAD Fixed VHF DSC Radio RD68

Service Manual



# SIMRAD Fixed VHF DSC Radio RD68

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# SIMRAD Fixed VHF DSC Radio RD68

Introduction

# 1 INTRODUCTION TO THE SIMRAD RD68 FIXED DSC VHF RADIO

The Simrad RD68 is a combined VHF radio, watch-keeping receiver and Class D Digital Selective Calling (DSC) unit to facilitate routine and distress calling on VHF Channel 70. Digitally Selected Calls are quicker and simpler to make than traditional voice calls using Channel 16 and should a distress situation occur, an alert can quickly be raised indicating identity, position and nature of the emergency and automatically establish communication on the emergency voice channel. The RD68 is robustly constructed using a pressure die cast aluminium case for effective heat dissipation ensuring maximum transmission performance even after many hours of constant use.

The RD68 has full international channel capability; 16 pre-programmed private channels; features Dual Watch, Tri Watch, Scan and full memory operation; back-lit LCD display and is available with either fist-mic or telephone handset.

The main components of the Simrad RD68 are:

# 1.1 Electronics PCBs

1.2

a.	Receiver / Transmitter PCB	Drawing No. E03866						
b.	Control PCB	Drawing No. E03656						
c.	Second Receiver PCB	Drawing No. E03211						
d.	Fist Mic	Drawing No. E03283						
e.	Telephone Handset	Drawing No. E03308						
Mechanical Components								

a.	RD68 Chassis	Drawing No. E03848
b.	RD68 Assembly	Drawing No. E03847
C.	Fist Mic Assembly	Drawing No. E03161
d.	Telephone Handset Assembly	Drawing No. E03162



# SIMRAD Fixed VHF DSC Radio RD68

Operation

# 2 OPERATING THE SIMRAD DSC VHF RADIO

This Service Manual only contains operating instructions for those features of the Simrad RD68 Radio that are not normally available to the end user. For details of normal operation please refer to the Simrad RD68 Instruction Manual, E03912.

**LCD Test Mode**. The LCD test mode may be entered by holding Soft Keys 2 and 4 on power up. Depressing each key in turn will then fill the display with the appropriate characters. The radio must be turned off to exit test mode.



# SIMRAD Fixed VHF DSC Radio RD68

# Assembly Instructions

# **3** ASSEMBLY INSTRUCTIONS

# 3.1 RD68 Chassis Pre-Assembly

The main transmitter and receiver and second receiver PCBs are a matched pair and replacement of either requires the tuning of both to be checked and adjusted as necessary. Position the PCB over the Chassis.



Apply a small amount of Hellerman sleeving oil to the part of the Red and Black power and the red and blue NMEA leads inside the chassis, to provide lubrication and facilitate withdrawal from the chassis as the board is set down. Ensure that the 2 copper tags from the washer on the Antenna socket are standing up clear and insert the tinned copper wire from the rear of the Antenna socket up through the plated hole in the board. Fit the PCB into the chassis, carefully pulling the 4 leads through the grommet to remove excess from the chassis interior. Locate the PCB over the ribs in the chassis and push down to position the PCB flat into the casting. When the board is correctly fitted, refer to drawing No E03848 and fit 2 - M3 X 10 screws 200048 into the front of the chassis to secure the heatsink in place, <u>do not fully tighten these screws at this stage</u>.

Fit 1 washer 200081 onto each of the 2 screws M3 x 16mm 200200, and fit into the 2 holes at the back of the chassis. When all 4 screws are in place, lightly press down on the front part of the PCB until the front panel connectors are clearly visible and permit engagement of the front panel without interference. (See drawing below)



When this has been achieved fully tighten the rear module screws and then the heatsink screws at the front.

Solder the tinned copper wire from the Antenna socket into the board, then fold down the 2 tags, from the Antenna socket copper washer, onto the PCB and solder both tags down to the pads.

# 3.2 RD68 Final Assembly

Fit the Cover Seal E03117 into the top cover ensuring that the seal lays into the recess around the cover. Refer to drawing E03847, place Screen / Insulator Assembly E03223 on top of the PCB in the Chassis Assembly E03848 with the Leatheriod insulator down towards the board. Fit Top Cover onto Chassis assembly and fix down using 5 off M3 x 16 Pan Head Slot Screws fitted with M3 Nyltite Sealing Washers 200252, fully tighten ensuring that the seal remains correctly fitted. Refer to drawing E03849 and fit the Front Panel Seal E03116 to the Front Panel Assembly E03850 as shown, ensuring that the 6 small 'feet' attached to the seal sit down onto the PCB. Take the four sets of wires from PLUGS 1 - 4 on the front panel and, referring to the drawing below, plug them into their correct connectors as shown.



Offer up the front panel to the chassis and, ensuring that the seal is still correctly located, fit the front panel. Refer to drawing No.E03847 and onto each of the four No.6 x 1/2" Pan Head Screws 200005, fit a M3.5 Nyltite Washer 200253. From the rear, fit these screws through the chassis and into the front panel. Screw on the front panel, fully tightening the screws.

# 3.3 Fist Mic Assembly

Refer to Drawing No. E03161 and fit the PTT Grommet E03143 into the web in the front case ensuring that it is correctly seated. From the end of the Cable Assembly E03175 strip and remove the outer insulation for 15mm. Cut off the Blue, Green, Orange and Black wires, (not used), flush with the end of the outer insulation. Strip 5mm of insulation from the ends of the Red and White wires, twist and sleeve the screen and tin the ends. Pass the end of the cable up through the bottom hole in the front case, apply a small quantity of Hellerman Sleeving Oil to the cable and slide on Cable Grommet E03141. Pull the cable and grommet through together, until the grommet is fully home, ensuring that the flat on the head of the grommet lays against the inside of the case, then pull cable back through grommet by approximately 100mm. Wipe off any excess sleeving oil from the cable. Solder the cable connections to the PCB and position the PCB assembly onto the 2 mounting pillars. Route the cable into the case, pulling out any excess wire, until the PCB and cable lay neatly into the case. Ensure the PTT Key hits the push switch before fixing the PCB assemble into the case using 2 screws (200036).

Into the groove around the outside of the Rear Case E03133 fit a Case Seal E03136 ensuring it is pushed fully home. Fit the PTT Key E03134 so that the spring leg in the centre of the key with a guide either side, faces out, and the peg on the other side of the key faces into the case front. Fit the rear case onto the front using washer (200257) and screw (200023) and tighten firmly.

# 3.4 Telephone Handset Assembly

Refer to Drawing No. E03162 and fit the PTT Grommet E03143 into the side wall of the front case ensuring that it is correctly seated. From the end of the Cable Assembly E03175 strip and remove the outer insulation

for 35mm. Cut off the Blue wire flush with the outer insulation, (not used). Cut back the Red wire to 20 mm long from the end of the outer insulation, the Orange and Black wire to 30 mm and the Green and White wire to 35 mm. Strip and tin the ends. Pass the end of the cable up through the hole in the bottom of the front case, apply a small quantity of Hellerman Sleeving Oil to the cable and slide on Cable Grommet E03141. Pull the cable and grommet through together, until the grommet is fully home into the case, ensuring that the flat on the head of the grommet is facing out, not towards the wall of the case. Then pull cable back through grommet by approximately 30mm. Wipe off any excess sleeving oil from the cable. Secure with Cable Tie 200026. Fit the handset weight E03311 and hold in place by fitting 3 Grommets 190024 onto the pillars. Solder the five wires and screen from the coiled cable and the two wires from the speaker to the PCB E03309. Position the PCB into the case on top of the three pillars retaining the weight and using 3 Screws 200082, fix the PCB into the case. Fully tighten the 3 screws. Fit PTT Key and check that the key operates the switch. Into the groove around the outside of the Rear Case E03140 fit Case Seal E03142 ensuring it is pushed fully home. Fit the rear case onto the front, ensuring the seal remains in place, using 6 Screws 200055 with washers and tighten screws firmly to secure front and rear case halves together.



# SIMRAD Fixed VHF DSC Radio RD68

Mechanical Assembly Drawings

# 4 MECHANICAL ASSEMBLY DRAWINGS

4.1	Assembly Chassis	E03848
4.2	Assembly PCBs & Power Module	E03867
4.3	Assembly Mobile Radio	E03847
4.4	Assembly Front Panel	E03849
4.5	Assembly Fist Mic	E03557
4.6	Assembly Telephone Handset	E03558



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i		ICE WITH BS 308				Drg. No.: Product Used on:	EO3 Group:	867 811 E03848	•
A		E03772	-/			<u> </u>			^
			- E		<b>N</b>				
8									8
	E03303 "TAB"				`# <b>**</b> ©				
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# SIMRAD Fixed VHF DSC Radio RD68

# Circuit Descriptions

# 5 CIRCUIT DESCRIPTIONS

# Introduction

The SIMRAD RD68 consists of 3 PCB assemblies. The main Receiver / Transmitter PCB assembly, Navico Part No. E03866, contains all the transmitter and receiver circuitry including the synthesiser, modulator and audio power amplifiers. The control PCB assembly, Navico Part No. E03656, houses the microcontroller, user controls and interfaces, display module and ATIS detection and mute circuitry. A full Class D DSC controller is also incorporated through a V.23 modem. The second receiver, Channel 70 watchdog receiver, Navico Part No. E03211, is fitted to the main Tx / Rx assembly, the 2 PCBs are an interdependent matched pair.

# 5.1 Receiver / Transmitter PCB Assembly

Refer to drawing number E02955.

**Receiver.** RF from the Antenna socket passes through the low pass PA filter to a guarter wave match / switch L5, L6 and pin diode D1. During transmissions, D1 is switched on to protect the receiver. There then follows an optional attenuator and power splitter, to provide RF to the second receiver in the RT1400, which is bypassed by link LK1 in the C214 is a 1nF coupling capacitor and L7, L8 and the RT1200. associated capacitors form a bandpass filter at the input to the RF preamplifier TR1. L9 and L11 form a further bandpass section before the mixer TR2. The RF signal is fed to TR2 Source and the local oscillator to Gate 1. Low side injection is used, i.e. the LO, TR105 buffered by TR103 and TR104, is 21.4 MHz below the receive frequency. L13 forms the drain load before matching through C25 and C26 to the 4 pole crystal filter XTAL1 and XTAL2. The output of the filter is coupled by L14, C28 and C29 to the integrated IF amplifier / demodulator IC1. A second local oscillator running at 21.855MHz produces the second IF of 455KHz. XTAL4 is a 6 pole ceramic filter completing the necessary bandwidth definition with quadrature coil L16 providing demodulation.

Audio from pin 9 of IC1 is de-emphasised by R20 and C43, buffered by TR20 and distributed to the various audio stages on the control PCB. The audio from pin 9 is also passed through a very high gain amplifier, within IC1 and configured as a bandpass filter, to detector D2 to provide a voltage proportional to the received signal strength. This level is fed to the front panel to provide squelch control. The AF signal is returned from the control panel after the volume and squelch controls to 2 audio power amplifiers in bridge mode. The internal speaker output remains live when an external speaker is connected.

**Transmitter**. The LO switch / buffer TR102 switches the transmit signal for amplification by controlled transistors TR101 and TR100 to drive the hybrid PA module IC100 which is capable of generating the required 25 Watts. The output is switched through PIN diode D100 before passing

through the PA filter L1 to L4. L4 of this filter, with diodes D101 and D102, forms a simple forward and reverse power detector to provide power control and transmitter status indication on the front panel display. The power control signal is fed to differential amplifier IC101 together with the reference power signal from VR100 and VR101, to set the high and low power levels. The output of this amplifier forms a regulated supply with TR113 and TR114 to supply the PA drivers TR100 and TR101. The reference input and supply for TR114 is fed from TR111 which is only turned on after the synthesiser is in lock.

**Local Oscillator / Synthesizer**. The main LO consists of TR105 connected as a Colpitts oscillator. Tuning is by L113, varactor VC102 and VC104. Band switching between receive and transmit is by PIN diode D103. The output of the oscillator is buffered by a cascade pair formed by TR104 and TR103 in receive and TR102 and TR104 in transmit., the supply to TR102 and TR103 being switched between the 2 modes. The output is also buffered by TR118 to the input of the synthesiser IC105. The reference frequency at 9.6 MHz is generated by TR119 and controlled by XTAL101. C200, C115 and R157 form the main loop filter with additional suppression of the reference frequency by R179 and C204. The synthesiser is controlled from the control panel via a 3 wire serial interface.

**Modulator**. Audio from the control panel is amplified by IC104b, the gain being configured by R169, R173 and C169, to give the signal preemphasis. The output of the stage is peak detected by D3 and detector TR1167 to provide a gain control signal to TR117 to enable limiting of high level input signals. Temperature compensation of the limiter is provided by thermistor TH1 and IC104a forms a high pass filter to meet the 14dB / octave roll-off above 6kHz. The final output is fed to the modulation diode VC101 via gain control VR102 which is set to a maximum of 5kHz deviation.

**Power Regulation and Switching**. The 12 volt supply is switched on the front panel and then feeds the audio amplifiers and regulators. The RF power module takes power before the switch to minimise voltage drops. D104 and D105 provide reverse polarity protection. A regulated 8v supply is provided by IC103 and 5v from IC107. The supplies for receive and transmit circuits are switched by TR106 and TR110 controlled form the synthesiser via TR107 to TR109. IC105 controls the switching of PIN diode D103 ensuring that there is a suitably high reverse bias across it in the OFF condition.

**NMEA Reception**. (RD68 only) Position and time information, from a GPS unit, is received in the form of NMEA data. This is opto-coupled into the radio via R225, D5 and IC4. The data is then routed through to the control PCB via PLG3.



# 5.2 Control PCB Assembly.

Refer to drawing number E03656.

All the functions of the radio are controlled from this assembly by the microprocessor IC4. The microprocessor has it own clock controlled by XTAL1 running at 7.15909MHz. Reset generator IC9 ensures that the microprocessor starts up correctly and resets under low voltage conditions. The microprocessor has a data bus interface driving the front panel display module. External controls consist of a 21 push button key matrix, rotary squelch and volume control VR1 and VR3 respectively. The level of illumination on the LCD and keypad is controlled by TR7 driving LED's 1 to 10 and the integral LED's on the LCD module.

Configuration data, MMSI and channel information is stored in the non volatile memory IC5. This interfaces to IC4 via a 4 wire serial interface shared with the synthesiser data to the Rx / Tx PCB assembly. Separate enables ensure that the data is routed correctly.

Volume control VR3 controls the level of audio in the loudspeaker. Amplifier IC2a boosts the level delivered to the handset earpiece. Individual mutes of the handset and speaker audio are provided by TR3 and TR4 respectively, under control of the microprocessor. The voltage on the squelch control and noise input from the Rx/Tx PCB are read and the audio muted as appropriate. Additionally the state of the handset is detected from SKT1 to mute the speaker when the handset is off cradle (optional).

The handset interface is via connector SKT1. The microphone input is biased for use with Electret microphones and may be muted by IC1a. The 12V output is current limited by TR5 and TR6. The cradle and PTT lines are also used for external data connections to a PC or third party accessories.

Data interfaces are provided for external programming via TR1 and TR2. NMEA data is received from the Rx/Tx PCB via PLG2 and passed onto the microprocessor, IC4, via IC1c.

Optional circuitry for ATIS detection consists of IC6, IC7, D1 and TR8.The inclusion of IC7 allows the ATIS signal to be muted during reception. The received audio is filtered by IC6c and IC6d which are followed by a zero crossing detector formed by IC6a, IC6b, D1 and TR8. This signal is sent to a second microprocessor, IC7, which measures the period of each half cycle of the incoming signal. By counting the number of periods which might be an ATIS signal the micro can decide whether ATIS is being received or not. When this decision is made, after about 10ms, IC7 sets an output to inform IC4 to mute the audio for 300ms. IC7 derives its clock and reset from the main microprocessor IC4.

The ATIS and DSC signals are generated and decoded by the modem, IC8 and surrounding components. TR9 switches the modulation index between that required for DSC and ATIS. The output signal is then passed onto the microphone audio via IC1b. Note that the microphone is muted by IC1a during transmission of the ATIS or DSC signals.

The audio level required for alarms is controlled by IC1d. For normal key 'beeps' the microprocessor, IC4, generates a square wave which is filtered and reduced in level by R15, R75, C14 and C105 and then fed into the audio amplifier via PLG1. For alarm generation IC1d short circuits R75 to increase the level of signal being fed to the audio amplifier.



# 5.3 Second Receiver PCB Assembly

Refer to drawing number E03209.

The second receiver is connected to the main receiver at the power splitter L10, C215, C214 and R205. The basic circuitry is the same as the receiver section of the main receiver. L300, L301 and associated capacitors form the input bandpass section, prior to the RF amplifier TR300. A second bandpass section is formed by L302 and L303, which couple into the mixer at the source of TR301. Low side injection from the local oscillator is fed into the gate. The output at 17.9MHz passes through the crystal filter XTAL300 and XTAL301 to the second IF stage, IC300. Demodulated audio is buffered by IC302a.

TR304 and XTAL304 form the reference oscillator for the synthesiser, IC301. Data from the front panel is fed from Clock, Data and Enable 2. C350, C355 and R345 form the basic loop filter to control the frequency of the local oscillator, TR303, by varactor VC301. The output of the local oscillator is buffered by TR302 before being coupled to the mixer through band bass filter L307 and L308.





# SIMRAD Fixed VHF DSC Radio RD68

Circuit Diagrams

# 6 CIRCUIT DIAGRAMS

# 6.1 Circuit Schematics

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308





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1 <b>9</b> 9p	CSS	Pini
1	1990011056	
19801	1007	
	100-11-00-0	- SQ PIN3
	1940 C28	(CS-SY2) PING
1980	C59	CS-SY PIN7
	1000 068	
1 <b>00</b> p	1061	
	108011062	
19Rot	1/67	(FHI-LO)PINI2
	100-11-0	CRAD PINI3
	. 16960 CE+	PIN17
1060	C65	-CATIS PINIS
	1990 1066	
1890	(C67	
	1880011068	
1960	1069	
	196011079	-COUT PIN22
100-1		-CIN PINZ3
LOOD	C/1	OSCIN PININ, 15. 24.
	10000 (272	
1960	C73 *1	
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1960	C74	
	1880 [ 175	
108p	<sub>676</sub>	
	198p C77	
1980	C78 II	
5	1000 1079	
100p	LC89	
1	1960 [ (81	
1980	1082 11	
1	1980 (83	
1960	C84 11	(LCDR/H) PIN
I	1980 085	
1980	11 cae	
	1980 1087	
1990	C88 11	
	1989 (89	OSCIND PIN
1994	C98 //	
- 1	100-101	PROSSED PIN
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				DRG No: E03283
MIC O RED				
PTT O			EM-4F MIC	
ØV O SCREEN				
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 	DRAWN BY: CHECK R.F. DATE ,28-10-97DATE;	ED: APPROVED: 2	25-6-98	CIRCUIT DIAGRAM



# Component Lists and Layouts

Receiver / Transmitter PCB Front Assembly Detail	E03866(Sht 1)
Receiver / Transmitter PCB Assembly Detail	E03866(Sht 2)
Receiver / Transmitter PCB Rear Assembly Detail	E03866(Sht 3)
Front Panel Assembly Detail (Sht 1)	E03658
ATIS Front Panel Assembly Detail	E03865
Second Receiver PCB Assembly Detail	E03211
Fist Mic PCB Assembly Detail	E03285
Telephone Handset PCB Assembly Detail	E03309



### (REF E03866:SM PCB ASSY SM:RX/TX)

ITEM

QTY

PART No.

SURFACE MOUNT COMPONENTS

### SURFACE MOUNT COMPONENTS COMP REF

DESCRIPTION

DRG No : EØ3866

ITEM	QTY	PART No.	COMP REF	DESCRIPTION
ı	1	-	E02956	PCB DRILLED
2	1	100125	R121	1206 10R
3	13	100161	R2.R14.R15.R102.R106.R110.R111.R155.R171.R175.R102.R209	0805 100K
L.	1	100152		6705 M
Ś	L L	100163	8179.8189.8202 8216	0005 2200
6	7	100166	R7.R21.R108.R107.R148.R149.R178	2805 47K
7	8	100167	R126.R142.R143.R173.R108.R103.R217.R218	9805 IK
8	3	100168	R19 .R163 .R204	0005 6K2
19	2	100169		0805 240R
11	L.	188175	P123, P133, P152, P222	00005 2K7
12	2	180177	R9 .R214	0003 470K
13	4	100101	R13. R129.R132.R136	0805 10R
14	1	100103	R115	9905 22K
15	2	199166	R4.R191	9605 1808
10	19	100108	R1.R28.R116.R122.R139.R131.R146.R165.R166.R219.R220	0805 4K7
19	2	108191	R9.8223	8885 18K
19	ũ	100193	R15, R125, R127, R151, R157, R164, R167, R174, R213, R219, R224	8885 252
28	Ł	100194	R176	0805 2M2
21	9	100195	R5.R6.R12.R135.R150.R193.R199.R201.R203	0805 47R
22	1	199195	R169	0805 478K
23	1	100197	N104	0905 2K
25	3	199294	R112-R128-R158	0803 IK3
26	1	108286	R153	8895 6888
27	1	100209	R141	0805 120K
28	6	109231	R185,R186,R187,R188,R192,R215	0805 4R7
29	1	100262	R198	0905 5KG
31	<u>د</u>	100203	0211	8685 338R
32	ī	100277	RII	0003 335
33	1	10029t	R172	0805 91K
34	2	100292	R18.R198	0805 3KG
35		109300	R109 ·	0885 GKB
30	3	100304	RC2/	0005 30R
39	ž	199338	R177.R207	9693 4/8
39	1	199342	R161	8683 2K2
40	2	100350	R139.R160	0603 10K
41	1	190355	Risi	0603 27K
	6	1100362		0603 100K
44	47	119115	C1.C14.C29.C23.C24.C39.C53.C54.C199.C192.C195.C197.C113	330F 23V ELECT
			C114.C118.C119.C123.C124.C125.C126.C131.C132.C154.C154 C168.C176.C188.C181.C191.C192.C193.C194.C197.C283.C224 C227.C228.C229.C234.C235.C236.C251.C252.C256.C257.C259 C256	0003 1.
* \$5	3	110116	C5.C21.C33	0005 22p
46	5	110110	C32.C34.C35.C206.C211	8885 1880
	÷.	110119	CB.C18.C15,C129	0805 15p
49 49	Ļ.	118125	C13.C186.C189.C128	0905 12p
59	2	110127	C+,C29	0805 27o
51	16	110120	C%5.C%6.C%9.C50.C51.C108.C1%7.C16%.C17%.C185.C109.C196 C198.C20%.C207.C258	2005 120n
52	1	110129		9805 39p
55	1	110131	C12.C13.C22.C153.C212	0005 47p
55	i i	110137	cii5	8885 80
56	6	110138	C41-C52-C151-C200.C233.C261	8985 180
57	1	110139	C26	8885 68p
58	1	110141	C36	0005 330p
59 Ca	3	110145	C111.C134.C186	0805 202
61	5	118165		0005 Sp6
62	ī	110152	C43	0003 3p3 0005 339n
63	<b>4</b>	110153	C2.C9.C11.C127	0905 18p
64	4	110166	C3.C19.C264.C267	0905 1p
65	6	110169	C42.C160.C165.C197.C208.C230	LUF SOV SM ELECT
67	2	110103	C157 C159	407 25V SM ELECT
58	3	110173	C5.C128.C133	0005 kg7
69	ī	118174	C27	0005 6a8
78	1	118181	C169	0905 L7n
71	ι	110185	C198	0905 220n
72	۲. ۱	110183	VC1.VC2.VC3.VC103	ERCJA 18p
73	3	110196	C37,C30,C167	0005 100n X7R

R23.R24.R25.D1(M1809) 0100(M1809).C39.C104. C201.C216.C231.C270 C273.C274 NOT FITTED

· · · · · · · · · · · · · · · · · · ·	7. 7577778 88 81283 85 85 85 85 85 85 85 85 85 85 85 85 85	7 3   2   1   1   3 2   4   1   1   3 2   4   1   1   1   1   1   1   1   1   1	118134 118139 118212 118213 118213 118213 128836 128836 128836 128836 128837 138853 138853 138853 138853 138855 138555 138555 138555 138555 1385555 1385555 1385555555555	C (135 C (44, ) C (137 C (136 C (143 D (143) D (143) D (143) C (143) D (143) D (143) C (143) D (143) C (143) T (141) T	C142.C144.C145. 138.C178 C141 .VC182.VC184 D182 .TR182.TR183.TR .TR182.TR183.TR .TR186.TR189.T1 .VR181.VR182 L115.L115.L117	C182.C183.C2 R128 184.TR185.TR R112.TR115	118	9693 In 9693 100 9693 102 9693 102 9693 102 9693 102 985157 9845157 9845157 9845157 9845157 984539 984539 984539 984539 98531 97935 97935 97935 97935 97935 97935 97935 97935 97935 97935 97935 97935 97337 1071237% ND53042 PC357 77786 96618 PC357 77786 96237 76785 972387 1085 PC357 972387 1085 PC357 972387 1085 PC357 972387 1085 PC357 972387 1085 PC357 972387 1085 PC357 972387 1085 PC357 972387 1085 PC357 972387 1085 PC357 972387 1085 PC357 972387 1085 PC357 972387 1085 PC357 972387 1085 PC357 972387 1085 PC357 972387 1085 PC357 972387 1085 PC357 972387 1085 PC357 PC	
				_C0	VENTIONAL MOUN	COMPONENTS			
	ITEM	QTY	PART No.		COMP R	EF		DESCRIPTION	
	185 186 197 198 198 199 199 199 111 112 112 113 114 115 117 116 117 116 117 116 117 116 119 121 128 122 122 122 122 122 122 122 122	13111111121122122152162122	100115 110015 1200	TH1 C49.C C148 D185 D185 C189 C189 C189 C189 C189 C189 C189 C189	48.C151 .XTAL 2 PLG2 PLG4 L1.L2.L3.L4 .L104.L106.L100 L102 .L9.L11.L109.L1	19		SK NTC THERHISTOR IBOLF Sev ELECT LTOBUF Sev RADIAL E INS01 I	uH-1 uH-1 armed coil SinH armed coil 120nH hir cored coil 120nH l able coil 120nH cored coil 120nH cored coil 56nH
DRAIN BY: R.F. 6415.17-05-1	Снескер: 10 ран - 3 с/	Z/L = DATE	WED: AU 2017/00	SIMRO B A KONGSBERG COMPAN	ALE IF IN I STAR LANE, MARGAT TEL: ONE S 200230 MATERIAL / DATE: 22-06-00	DOUBT ASK E. KENI CT9 NNP EAK: 98%3 208%11 SCALE :	NO ISS THIS OWNI MY NO INVAL MODEL TITLE, RX/T ASSE	DATE HODIFICAT NO IS COMPUTER GENERATED UNA MODIFICATION HILL INDATE THE C.A.D. FILE RD68W X PCB (FRONT MBLY DETAIL	LDMS LDM175 FEF LIM(T5 FPP - K









### SURFACE MOUNT COMPONENTS

ITEM	QITY	PART No.	COMP REF	DESCRIPTION
1	1	100176	R3	0805 3K9
2	1	100163	R225	0805 220R
د.	1	100190	R226	0805 10K
Ļ	35	110115	C17.C104.C110.C171.C172.C173.C175.C205 C213.C219.C221.C222.C223.C225.C226.C237 C238.C239.C240.C241.C243.C244.C245.C246 C247.C248.C249.C250.C255.C262.C263.C265 C266.C268.C275	0805 in
5	3	110128	C149 .C242.C271	0805 100n
6	1	110138	C254	0805 10n
7	1	120036	D5	BAS16

					DO NOT	SCALE	IF IN DOL	BT ASK	No	ISS DAT	E MD01F1CA	TIONS		
C253,C269	.R23.R24,R25 NOT FITTED				$\square$	STAR LANE	. MARGATE, KE 3 299299 1	NT CT9 4MP	THIS DI ANY	RAWING IS C	COMPUTER GENERATED	LIMITS MET	LIMITS	IMP -
NOTE:- R2	26 CONNECTED BETWEEN ANTENNA PAD AND TP	11 (GND) AFT	FR PCB TES	т	$\mathbf{A}$	MATERIAL	3 298290	8943 2384 /1	IN MODEL :	VALIDATE TI	HE C.A.D. FILE		FRAC	.015
					പ്രട് പ്					RD	68W	00 - 10	. 669	.885
			·······			IN THE						ANG - 10.5	ANG -	0.5
		DRAWN BY	CHECKED:	APPROVED :		UATET	SCA	LE :	TITLE	:		DRG Na:	1	ISSUE
		R.F.	WAC .	AUS		22-01	6-00		RX/	TX PC	B REAR	E0386	6	1
		DATE :17-05-0	DATE : Co d 20-4	DATE 20/7/0	90 4	U			ASS	SEMBLY	DETAIL	SHT 3 0	F 3	•



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R71

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DRG Na : E03658

LIMITS INF

+ 8 -

FRAC . .015

-90 = -910

.000 - .005

ANG - 8.5

ISSUE

1









n





ATE 26-3-98

EØ3285

ASSEMBLY DETAIL

1



### CONVENTIONAL MOUNT COMPONENTS

ITEM	QTY	PART NO.	COMP REF	DESCRIPTION
1	1	E03174	_	PCB DRILLED
2	1	100044	R1	0.25W 4K7
3	1	100048	R2	0.25W 10K
ų.	2	110065	C1.C2	CER DISC in
5	1	120017	ZD1	BZX79
6	1	130000	TR1	BC182
7	1	160012	MIC	EM-4F
8	1	200276	-	MIC SPACER
9	1	210027	PTT	PUSH SWITCH TSH62A
10	1	210032	S1	REED SWITCH TM13101

DO N	OT SCALE	IF IN DOUBT ASK	1393 2	05-10-99	MIC SPACE	R ADDED		
ND COMPLUTICATION	STAR LAN TEL: 0843 +64 843	NE. MARGATE. KENT CT9 &NP 5 298290 TELEX 965893 NAVICO G 299290   FAX: 00%3 290%71 AL :	NO ISS THIS DRAWI ANY MAN INVALI MODEL : RT	DATE NG IS COMPUTED UAL MODIFICAT DATE THE C.A. AXIS	MODIFICATI R GENERATED ION WILL D. FILE +00	ONS LIMITS MET + 8 - 0. = .50 0.0 = .25 .00 = .10 ANG = 0.5	LIMITS + 8 FRAC = .000 = .000 = ANG =	5 IMP .015 .010 .005 0.5
R.F.	05-1	10-99	TITLE : TE ASSE	LE - H/ MBLY DE	'SET TAIL	EØ330	99	155UE : <b>2</b>



# SIMRAD Fixed VHF DSC Radio RD68

# Programming and Configuration

# 7 PROGRAMMING AND CONFIGURATION

# Introduction

All Transmitting / Receiving characteristics of the RD68 Series radiotelephones are stored in Non Volatile Memory (NVM). The NVM of any RD68 Series radio can be programmed with new characteristics using the programming kit, Part Number PR68.

The following features are available:

COUNTRY	Selects primary mode. NVMs with INT (International) selected for the FIRST COUNTRY flag will receive on the INT Rx frequencies. The radio can be switched to USA with weather channels if the BACKLIGHT key is depressed on power up unless the SELECTED MODE ONLY flag is set. (SECOND MODE – YES).
SECOND MODE	Enables or disables switch to USA with weather channels.
SCANNING	Enables or disables SCAN and MEMORY SCAN functions.
CHANNEL 10 SCAN	Enables or disables Channel 10 from SCAN function.
DUAL WATCH	Enables or disables DUAL WATCH function.
TRI WATCH	Enables or disables TRI WATCH function.
ALTERNATE SCAN	Enables or disables ALTERNATE SCAN function. Switches to Channel 16 between each channel when scanning. For use in Scandinavia only. $(1 - 16, 2 - 16, 3 - 16, 4 - 16)$ .
FIRST KEY BEEP	Enables or disables confirmation key beep from all first level functions. (NB. Second level key beep cannot be disabled).
STARTUP CHANNEL	Selects channel to be displayed on power up, usually Channel 16.
WATCH CHANNEL	Selects alternate channel to be monitored when DUAL WATCH selected.
USA / CANADA WEATHER CHANNELS	Enables or disables USA weather channels.

DSC IDENTIFIER (MMSI)	Stores Maritime Mobile Service Identity (MMSI) number.
ATIS IDENTIFIER	Stores Automatic Transmission Identification System (ATIS) Number.

### **INITIAL SET UP**

The PR68 programming kit operates in conjunction with an IBM compatible PC. The programme should be copied to the 'C' drive on the PC hard disc into a directory entitled **RD68DATA** 

If using a monochrome monitor type **MODE BW80**. Connection between the radio and PC is by means of the programming lead supplied with the kit from the radio Mic Socket, on the front of the radio, to a 9 pin serial port on the PC.

Enter the programming set up software by typing "Prog68". Place the radio into programming mode by holding Soft Keys 1 and 3 and the LIGHTS Key whilst powering up. Programming is entirely menu driven and self-explanatory.

Using COM1 port on computer	NVM Data: NOT LOADED
Select option:-	
<ol> <li>Load NVM data from Radio</li> <li>Load NVM data from File</li> <li>View/Modify NVM data</li> <li>Save NVM data to Radio</li> <li>Save NVM data to File</li> <li>CLONE from existing Radio</li> <li>Exit Program</li> </ol>	<ul> <li>(Copy data from radio to PC)</li> <li>(Load data from programme file)</li> <li>(View/change displayed data)</li> <li>(Save displayed data to radio)</li> <li>(Save data to new PC file)</li> <li>(Copy existing data from radio to radio (Exit programme)</li> </ul>

### Load NVM data from Radio

Connect Radio, press any key when ready

				NVM da <del>ta</del>			
00:	10:	20:	30:	40:	50:	60:	70:
01:	11:	21:	31:	41:	51:	61:	71:
02:	12:	22:	32:	42:	52:	62:	72:
03:	13:	23:	33:	43:	53:	63:	73:
04:	14:	24:	34:	44:	54:	64:	74:
05:	15:	25:	35:	45:	55:	65:	75:
06:	16:	26:	36:	46:	56:	66:	76:
07:	17:	27:	37:	47:	57:	67:	77:
08:	18:	28:	38:	48:	58:	68:	78:
09:	19:	29:	39:	49:	59:	69:	79:
0A:	1A:	2A:	3A:	4A:	5A:	6A:	7A:
0B:	1B:	2B:	3B:	4B:	5B:	6B:	7B:
0C:	1C:	2C:	3C:	4C:	5C:	6C:	7C:
0D:	1D:	2D:	3D:	4D:	5D:	6D:	7D:
0E:	1E:	2E:	3E:	4E:	5E:	6E:	7E:
OF:	1F:	2F:	3F:	4F:	5F:	6F:	7F:

Any key to continue, [ESC] to exit

### Load NVM data from Radio

Failed – check connections, is radio turned on?

				NVM da <del>ta</del>				
00:	10:	20:	30:	40:	50:	60:	70:	
01:	11:	21:	31:	41:	51:	61:	71:	
02:	12:	22:	32:	42:	52:	62:	72:	
03:	13:	23:	33:	43:	53:	63:	73:	
04:	14:	24:	34:	44:	54:	64:	74:	
05:	15:	25:	35:	45:	55:	65:	75:	
06:	16:	26:	36:	46:	56:	66:	76:	
07:	17:	27:	37:	47:	57:	67:	77:	
08:	18:	28:	38:	48:	58:	68:	78:	
09:	19:	29:	39:	49:	59:	69:	79:	
0A:	1A:	2A:	3A:	4A:	5A:	6A:	7A:	
0B:	1B:	2B:	3B:	4B:	5B:	6B:	7B:	
0C:	1C:	2C:	3C:	4C:	5C:	6C:	7C:	
0D:	1D:	2D:	3D:	4D:	5D:	6D:	7D:	
0E:	1E:	2E:	3E:	4E:	5E:	6E:	7E:	
OF:	1F:	2F:	3F:	4F:	5F:	6F:	7F:	

### Any key to continue, [ESC] to exit

NVM c	lata loaded s	uccessfully.	nen ready				
		5					
			N	VM data –			
00:1068	10:1110	20:0000	30:0000	40:9876	50:FFFF	60:FFFF	70:FFFF
01:3200	11:0BBB	21:0000	31:0000	41:5432	51:FFFF	61:FFFF	71:FFFF
02:B111	12:3333	22:0000	32:0000	42:10FF	52:FFFF	62:FFFF	72:FFFF
03:1101	13:3333	23:0000	33:0000	43:FFFF	53:FFFF	63:FFFF	73:FFFF
04:0000	14:3300	24:0000	34:0000	44:FFFF	54:FFFF	64:FFFF	74:FFFF
05:0004	15:3333	25:0000	35:0000	45:1919	55:FFFF	65:FFFF	75:FFFF
06:0411	16:3333	26:0000	36:0000	46:0606	56:FFFF	66:FFFF	76:FFFI
07:1111	17:3333	27:0000	37:0000	47:1010	57:FFFF	67:FFFF	77:FFFI
08:1111	18:33FF	28:0000	38:1016	48:FFFF	58:FFFF	68:FFFF	78:FFFI
09:1BB3	19:5431	29:DFD2	39:0010	49:FFFF	59:FFFF	69:FFFF	79:FFFF
0A:1111	1A:7232	2A:FFFF	3A:FFFF	4A:FFFF	5A:FFFF	6A:FFFF	7A:FFF
0B:1110	1B:0000	2B:FFFF	3B:FFFF	4B:FFFF	5B:FFFF	6B:FFFF	7B:FFF
0C:0030	1C:0000	2C:0000	3C:FFFF	4C:FFFF	5C:FFFF	6C:FFFF	7C:FFF
0D:0004	1D:0000	2D:0000	3D:FFFF	4D:FFFF	5D:FFFF	6D:FFFF	7D:FFF
0E:4011	1E:0000	2E:0000	3E:FFFF	4E:FFFF	5E:FFFF	6E:FFFF	7E:FFF
OF:1111	1F:0000	2F:0000	3F:0439	4F:FFFF	5F:FFFF	6F:FFFF	7F:FF83



Enter N	NVM Data 1	File Name:					
			;	NVM dat <del>a</del>			
00:	10:	20:	30:	40:	50:	60:	70:
01:	11:	21:	31:	41:	51:	61:	71:
02:	12:	22:	32:	42:	52:	62:	72:
03:	13:	23:	33:	43:	53:	63:	73:
04:	14:	24:	34:	44:	54:	64:	74:
05:	15:	25:	35:	45:	55:	65:	75:
06:	16:	26:	36:	46:	56:	66:	76:
07:	17:	27:	37:	47:	57:	67:	77:
08:	18:	28:	38:	48:	58:	68:	78:
09:	19:	29:	39:	49:	59:	69:	79:
0A:	1A:	2A:	3A:	4A:	5A:	6A:	7A
0B:	1B:	2B:	3B:	4B:	5B:	6B:	7B:
0C:	1C:	2C:	3C:	4C:	5C:	6C:	7C:
0D:	1D:	2D:	3D:	4D:	5D:	6D:	7D:
0E:	1E:	2E:	3E:	4E:	5E:	6E:	7E:
OF:	1F:	2F:	3F:	4F:	5F:	6F:	7F:

### Load NVM data from File

Enter NVM Data File Name: File cannot be found or cannot be opened

			NV	M dat <del>a –</del>			
00:	10:	20:	30:	40:	50:	60:	70:
01:	11:	21:	31:	41:	51:	61:	71:
02:	12:	22:	32:	42:	52:	62:	72:
03:	13:	23:	33:	43:	53:	63:	73:
04:	14:	24:	34:	44:	54:	64:	74:
05:	15:	25:	35:	45:	55:	65:	75:
06:	16:	26:	36:	46:	56:	66:	76:
07:	17:	27:	37:	47:	57:	67:	77:
08:	18:	28:	38:	48:	58:	68:	78:
09:	19:	29:	39:	49:	59:	69:	79:
0A:	1A:	2A:	3A:	4A:	5A:	6A:	7A:
0B:	1B:	2B:	3B:	4B:	5B:	6B:	7B:
0C:	1C:	2C:	3C:	4C:	5C:	6C:	7C:
0D:	1D:	2D:	3D:	4D:	5D:	6D:	7D:
0E:	1E:	2E:	3E:	4E:	5E:	6E:	7E:
OF:	1F:	2F:	3F:	4F:	5F:	6F:	7F:

Enter file name {and path if required) or [ESC] to exit

Enter N	VVM Data F	ile Name: R	D68DATA/	B1.DAT			
File loa	aded successi	fully.					
			N	WM data —			
00.1068	10.1110	20.0000	30.0000	10.9876	50.FFFF	60.FEFE	70.FFFF
01.3200	11:0BBB	21:0000	31.0000	41.5432	51.FFFF	61.FFFF	71·FFFF
02:B111	12:3333	22:0000	32:0000	42:10FF	52:FFFF	62:FFFF	72:FFFF
03:1101	13:3333	23:0000	33:0000	43:FFFF	53:FFFF	63:FFFF	73:FFFF
04:0000	14:3300	24:0000	34:0000	44:FFFF	54:FFFF	64:FFFF	74:FFFF
05:0004	15:3333	25:0000	35:0000	45:1919	55:FFFF	65:FFFF	75:FFFF
06:0411	16:3333	26:0000	36:0000	46:0606	56:FFFF	66:FFFF	76:FFFF
07:1111	17:3333	27:0000	37:0000	47:1010	57:FFFF	67:FFFF	77:FFFF
08:1111	18:33FF	28:0000	38:1016	48:FFFF	58:FFFF	68:FFFF	78:FFFF
09:1BB3	19:5431	29:DFD2	39:0010	49:FFFF	59:FFFF	69:FFFF	79:FFFF
0A:1111	1A:7232	2A:FFFF	3A:FFFF	4A:FFFF	5A:FFFF	6A:FFFF	7A:FFFI
0B:1110	1B:0000	2B:FFFF	3B:FFFF	4B:FFFF	5B:FFFF	6B:FFFF	7B:FFFI
0C:0030	1C:0000	2C:0000	3C:FFFF	4C:FFFF	5C:FFFF	6C:FFFF	7C:FFFI
0D:0004	1D:0000	2D:0000	3D:FFFF	4D:FFFF	5D:FFFF	6D:FFFF	7D:FFFI
0E:4011	1E:0000	2E:0000	3E:FFFF	4E:FFFF	5E:FFFF	6E:FFFF	7E:FFF
OF:1111	1F:0000	2F:0000	3F:0439	4F:FFFF	5F:FFFF	6F:FFFF	7F:FF83

View/Modify N Configuration	NVM data 1 Settings
Country: Second Mode Enabled: Scanning Enabled: Channel 10 Scan Enabled: Dual Watch Enabled: Tri Watch Enabled: Alternate Scan Enabled: User can disable first key beep: Startup Channel (SC): Watch Channel (WC): USA/Canada Weather Channels: DSC Identifier (MMSI):	INT NO YES YES YES NO YES 16 16 NO
$\leftarrow \uparrow \downarrow \rightarrow \text{select}  \text{[Enter] modify}$	[Pg Dn] done [ESC] exit

# $\boldsymbol{\mathsf{OR}}\xspace$ if radio is ATIS capable

NVM data	
n Settings	
INT	
NO	
YES	
YES	
YES	
YES	
NO	
YES	
16	
16	
NO	
[Pg Dn] done	[ESC] exit
	NVM data n Settings INT NO YES YES YES YES NO YES 16 16 16 NO 

Clear L	og and Dire.	ctory? {Y/N	1}				
			N	VM data			
00:1068	10:1110	20:0000	30:0000	40:9876	50:FFFF	60:FFFF	70:FFFF
01:3200	11:0BBB	21:0000	31:0000	41:5432	51:FFFF	61:FFFF	71:FFFF
02:B111	12:3333	22:0000	32:0000	42:10FF	52:FFFF	62:FFFF	72:FFFF
03:1101	13:3333	23:0000	33:0000	43:FFFF	53:FFFF	63:FFFF	73:FFFF
04:0000	14:3300	24:0000	34:0000	44:FFFF	54:FFFF	64:FFFF	74:FFFF
05:0004	15:3333	25:0000	35:0000	45:1919	55:FFFF	65:FFFF	75:FFFF
06:0411	16:3333	26:0000	36:0000	46:0606	56:FFFF	66:FFFF	76:FFFF
07:1111	17:3333	27:0000	37:0000	47:1010	57:FFFF	67:FFFF	77:FFFF
08:1111	18:33FF	28:0000	38:1016	48:FFFF	58:FFFF	68:FFFF	78:FFFF
09:1BB3	19:5431	29:DFD2	39:0010	49:FFFF	59:FFFF	69:FFFF	79:FFFF
0A:1111	1A:7232	2A:FFFF	3A:FFFF	4A:FFFF	5A:FFFF	6A:FFFF	7A:FFFI
0B:1110	1B:0000	2B:FFFF	<b>3B:FFFF</b>	4B:FFFF	5B:FFFF	6B:FFFF	7B:FFFF
0C:0030	1C:0000	2C:0000	3C:FFFF	4C:FFFF	5C:FFFF	6C:FFFF	7C:FFFF
0D:0004	1D:0000	2D:0000	3D:FFFF	4D:FFFF	5D:FFFF	6D:FFFF	7D:FFFI
0E:4011	1E:0000	2E:0000	3E:FFFF	4E:FFFF	5E:FFFF	6E:FFFF	7E:FFFF
OF:1111	1F:0000	2F:0000	3F:0439	4F:FFFF	5F:FFFF	6F:FFFF	7F:FF83

			N	VM data —			
00:1068	10:1110	20:0000	30:0000	40:9876	50:FFFF	60:FFFF	70:FFFF
01:3200	11:0BBB	21:0000	31:0000	41:5432	51:FFFF	61:FFFF	71:FFFF
02:B111	12:3333	22:0000	32:0000	42:10FF	52:FFFF	62:FFFF	72:FFFF
03:1101	13:3333	23:0000	33:0000	43:FFFF	53:FFFF	63:FFFF	73:FFFF
04:0000	14:3300	24:0000	34:0000	44:FFFF	54:FFFF	64:FFFF	74:FFFF
05:0004	15:3333	25:0000	35:0000	45:1919	55:FFFF	65:FFFF	75:FFFF
06:0411	16:3333	26:0000	36:0000	46:0606	56:FFFF	66:FFFF	76:FFFF
07:1111	17:3333	27:0000	37:0000	47:1010	57:FFFF	67:FFFF	77:FFFF
08:1111	18:33FF	28:0000	38:1016	48:FFFF	58:FFFF	68:FFFF	78:FFFF
09:1BB3	19:5431	29:DFD2	39:0010	49:FFFF	59:FFFF	69:FFFF	79:FFFF
0A:1111	1A:7232	2A:FFFF	3A:FFFF	4A:FFFF	5A:FFFF	6A:FFFF	7A:FFFI
0B:1110	1B:0000	2B:FFFF	3B:FFFF	4B:FFFF	5B:FFFF	6B:FFFF	7B:FFFI
0C:0030	1C:0000	2C:0000	3C:FFFF	4C:FFFF	5C:FFFF	6C:FFFF	7C:FFFI
0D:0004	1D:0000	2D:0000	3D:FFFF	4D:FFFF	5D:FFFF	6D:FFFF	7D:FFFI
0E:4011	1E:0000	2E:0000	3E:FFFF	4E:FFFF	5E:FFFF	6E:FFFF	7E:FFF
OF:1111	1F:0000	2F:0000	3F:0439	4F:FFFF	5F:FFFF	6F:FFFF	7F:FF83

Save NVM data to File Enter Data Ident: BRITISH NVM data 20:0000 30:0000 40:9876 00:1068 10:1110 50:FFFF 60:FFFF **70:FFFF** 41:5432 01:3200 11:0BBB 21:0000 31:0000 51:FFFF 61:FFFF 71:FFFF 02:B111 12:3333 22:0000 32:0000 42:10FF 52:FFFF 62:FFFF 72:FFFF 03:1101 13:3333 23:0000 33:0000 43:FFFF 53:FFFF 63:FFFF 73:FFFF 04:0000 14:3300 24:0000 34:0000 44:FFFF 54:FFFF 64:FFFF 74:FFFF 05:0004 15:3333 25:0000 35:0000 45:1919 55:FFFF 65:FFFF 75:FFFF 06:0411 16:3333 26:0000 36:0000 46:0606 56:FFFF 66:FFFF 76:FFFF 07:1111 17:3333 27:0000 37:0000 47:1010 57:FFFF 67:FFFF **77:FFFF** 08:1111 18:33FF 28:0000 38:1016 **48:FFFF 58:FFFF** 68:FFFF **78:FFFF** 39:0010 49:FFFF 09:1BB3 19:5431 29:DFD2 59:FFFF 69:FFFF **79:FFFF** 1A:7232 2A:FFFF 3A:FFFF 4A:FFFF 0A:1111 5A:FFFF 6A:FFFF 7A:FFFF 1B:0000 0B:1110 2B:FFFF **3B:FFFF** 4B:FFFF 5B:FFFF 6B:FFFF 7B:FFFF 0C:0030 1C:0000 2C:0000 3C:FFFF 4C:FFFF 5C:FFFF 6C:FFFF 7C:FFFF 0D:0004 2D:0000 1D:0000 3D:FFFF 4D:FFFF 5D:FFFF 6D:FFFF 7D:FFFF 0E:4011 1E:0000 2E:0000 3E:FFFF 4E:FFFF 5E:FFFF 6E:FFFF 7E:FFFF OF:1111 1F:0000 2F:0000 3F:0439 4F:FFFF 5F:FFFF 6F:FFFF 7F:FF83 Enter Ident Text [Enter] for none [ESC] to exit

			1 2000000				
			r				
00.1068	10.1110	20.0000	с 30.0000	40.9876	50.FFFF	60. FFFF	70.FFFF
01.3200	11:0BBB	21:0000	31.0000	41.5432	51.FFFF	61.FFFF	71.FFFF
02:B111	12:3333	22:0000	32:0000	42:10FF	52:FFFF	62:FFFF	72:FFFF
03:1101	13:3333	23:0000	33:0000	43:FFFF	53:FFFF	63:FFFF	73:FFFF
04:0000	14:3300	24:0000	34:0000	44:FFFF	54:FFFF	64:FFFF	74:FFFF
05:0004	15:3333	25:0000	35:0000	45:1919	55:FFFF	65:FFFF	75:FFFF
06:0411	16:3333	26:0000	36:0000	46:0606	56:FFFF	66:FFFF	76:FFFF
07:1111	17:3333	27:0000	37:0000	47:1010	57:FFFF	67:FFFF	77:FFFF
08:1111	18:33FF	28:0000	38:1016	48:FFFF	58:FFFF	68:FFFF	78:FFFF
09:1BB3	19:5431	29:DFD2	39:0010	49:FFFF	59:FFFF	69:FFFF	79:FFFF
0A:1111	1A:7232	2A:FFFF	3A:FFFF	4A:FFFF	5A:FFFF	6A:FFFF	7A:FFFF
0B:1110	1B:0000	2B:FFFF	3B:FFFF	4B:FFFF	5B:FFFF	6B:FFFF	7B:FFFF
0C:0030	1C:0000	2C:0000	3C:FFFF	4C:FFFF	5C:FFFF	6C:FFFF	7C:FFFF
0D:0004	1D:0000	2D:0000	3D:FFFF	4D:FFFF	5D:FFFF	6D:FFFF	7D:FFFF
0E:4011	1E:0000	2E:0000	3E:FFFF	4E:FFFF	5E:FFFF	6E:FFFF	7E:FFFF
OF:1111	1F:0000	2F:0000	3F:0439	4F:FFFF	5F:FFFF	6F:FFFF	7F:FF83

### Save NVM data to File

Enter NVM Data Filename: C:\MY DOCUMENTS\UK.DAT File written successfully

			E	BRITISH –			
00:1068	10:1110	20:0000	30:0000	40:9876	50:FFFF	60:FFFF	70:FFFF
01:3200	11:0BBB	21:0000	31:0000	41:5432	51:FFFF	61:FFFF	71:FFFF
02:B111	12:3333	22:0000	32:0000	42:10FF	52:FFFF	62:FFFF	72:FFFF
03:1101	13:3333	23:0000	33:0000	43:FFFF	53:FFFF	63:FFFF	73:FFFF
04:0000	14:3300	24:0000	34:0000	44:FFFF	54:FFFF	64:FFFF	74:FFFF
05:0004	15:3333	25:0000	35:0000	45:1919	55:FFFF	65:FFFF	75:FFFF
06:0411	16:3333	26:0000	36:0000	46:0606	56:FFFF	66:FFFF	76:FFFF
07:1111	17:3333	27:0000	37:0000	47:1010	57:FFFF	67:FFFF	77:FFFF
08:1111	18:33FF	28:0000	38:1016	48:FFFF	58:FFFF	68:FFFF	78:FFFF
09:1BB3	19:5431	29:DFD2	39:0010	49:FFFF	59:FFFF	69:FFFF	79:FFFF
0A:1111	1A:7232	2A:FFFF	3A:FFFF	4A:FFFF	5A:FFFF	6A:FFFF	7A:FFFF
0B:1110	1B:0000	2B:FFFF	3B:FFFF	4B:FFFF	5B:FFFF	6B:FFFF	7B:FFFF
0C:0030	1C:0000	2C:0000	3C:FFFF	4C:FFFF	5C:FFFF	6C:FFFF	7C:FFFF
0D:0004	1D:0000	2D:0000	3D:FFFF	4D:FFFF	5D:FFFF	6D:FFFF	7D:FFFF
0E:4011	1E:0000	2E:0000	3E:FFFF	4E:FFFF	5E:FFFF	6E:FFFF	7E:FFFF
OF:1111	1F:0000	2F:0000	3F:0439	4F:FFFF	5F:FFFF	6F:FFFF	7F:FF83

Any key to continue

# RD68 RADIO CONFIGURATION PROGRAM ------ Using COM1 port on computer NVM Data: LOADED Select option: 1 - Load NVM data from Radio 2 - Load NVM data from File 3 - View/Modify NVM data 4 - Save NVM data to Radio 5 - Save NVM data to File 6 - CLONE from existing Radio 7 - Exit Program

Use  $\uparrow \downarrow$  [Enter] or type option number, type [ESC] to exit program



		View / Modify NVN	I data	
		Aux Channel Sett	ings	
Channel	Att	ributes	Display	Frequency (MHz)
A1	-	S	M	157.850
A2	-	S	M2	161.425
A3				0.000
A4				0.000
A5				0.000
A6				0.000
	() disabled	(T) Receive only	(L) Low power only	
	(S) Simplex	(D) semi-Duplex	(R) Reverse duplex	
$\leftarrow \uparrow \downarrow \rightarrow$ select	[Delete] clear	[Enter] modify	[Pg Dn] done	[ESC] exit

NB. When entering frequency, only the transmit frequency is requested for entry, the receive frequency is entered automatically by the programme regardless whether simplex, duplex or reverse duplex is required provided that the attributes have been set.

	View / Modify N	VM data					
Private Channel Settings							
Channel	Attributes	Frequency (MHz)					
PO		0.000					
P2		0.000					
P3		0.000					
P4		0.000					
P5		0.000					
P6		0.000					
P7		0.000					
P8		0.000					
Р9		0.000					
Passwo	ord protection is OFF u	se [F4] key to change					
() disabled	d (T) Receive only	(L) Low power only					
(S) Simplex	(D) semi-Duplex	(R) Reverse duplex					
$\leftarrow \uparrow \downarrow \rightarrow$ select [Delete	e] clear [Enter] r	nodify [Pg Dn] done	[ESC] exit				



# SIMRAD Fixed VHF DSC Radio RD68

Fault Finding

# 8 FAULT FINDING

8.1 Common User Faults

None Yet Identified.

8.2 Common Technical Faults

None Yet Identified



# SIMRAD Fixed VHF DSC Radio RD68

Spare Parts Detail

# 9 SPARE PARTS DETAIL

# 9.1 Spares Packs

Part No.	Description
RTPK18	Front Panel Assembly
RTPK18:A	Front Panel Assembly ATIS Version
RTPK19	Front Panel PCB
RTPK19:A	Front Panel PCB ATIS Version
RTPK20	Front Panel Assembly (Minus PCB)
RTPK21	PCBs & Power Module Assembly

Items common to RT1200, RT1400 and RD68

Part No.	Description
RTPK9	Bottom Cover Kit
MB1000:BK	Standard Mounting Kit – Black
RTPK12	Power Module Kit
RTPK13	Chassis Assembly
RTPK15	Accessory Kit
THS4:SIM	Telephone Handset – Black
CRDL1:BK	Handset Cradle – Black
FTM5:SIM	Fist Mic Assembly – Black

# 9.2 Service Aids

Part No.	Description	
PR68	Programming Kit	



# SIMRAD Fixed VHF DSC Radio RD68

Technical Notes

# 10 TECHNICAL NOTES

None Yet Issued